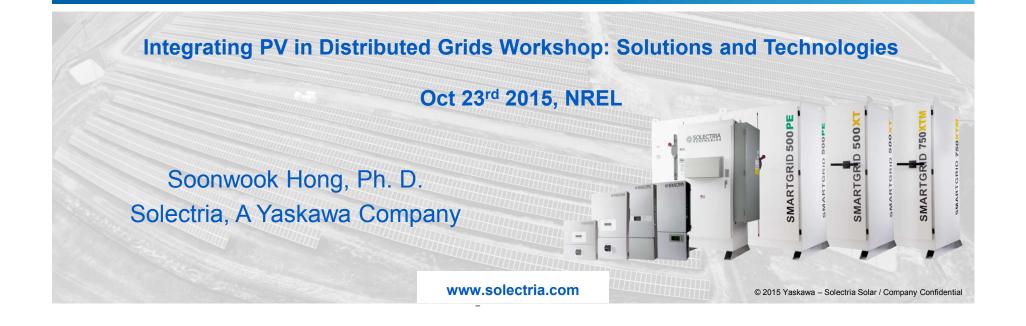
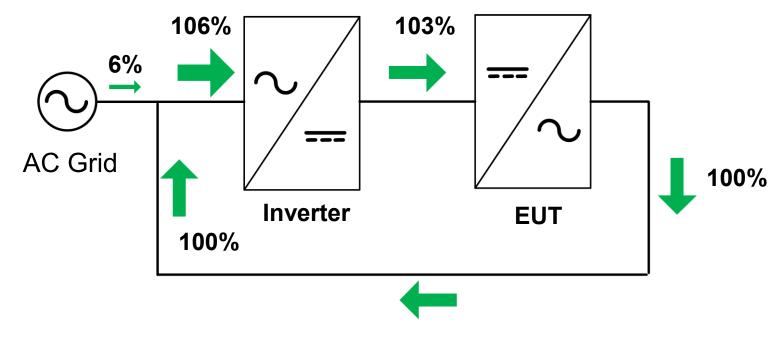


Certifying DERs for the Grid: Utility Scale Inverter Testing





Conventional Inverter test: Operational Test



Back-to-Back Configuration

Very effective (efficient) way of testing inverter at full load condition

Limitation: Tests are done with the given AC grid condition





Abnormal Grid V/F Test: Protection functions



Logic level tests running at extreme temperature conditions

Exception: Anti-islanding and Open phase test are done with the actual grid





Industry Trends

Conventional UL/IEEE standards

- Focus on generation only
- Mostly safety related
- Logic level Protection test
- Individual Inverter operation

New standards (Rule 21, New IEEE, IEC)

- Mitigation plans for high penetration issues (reactive power, ride through operation,...)
- Performance oriented how smart? how fast? how accurate?
- Much more detailed requirements for industry compliances.

UL1741SA Draft for ride through test:

"The input source shall be capable of supplying 150% of the rated input current of the EUT"





Input source requirement for future inverter test



NREL ESIF provides a great option for testing utility scale inverters.

Also, excited to hear that NREL opens the facility for certification test!





Continuous Partnership since 2013



Started with DoE SunShot Initiative project with EPRI leadership





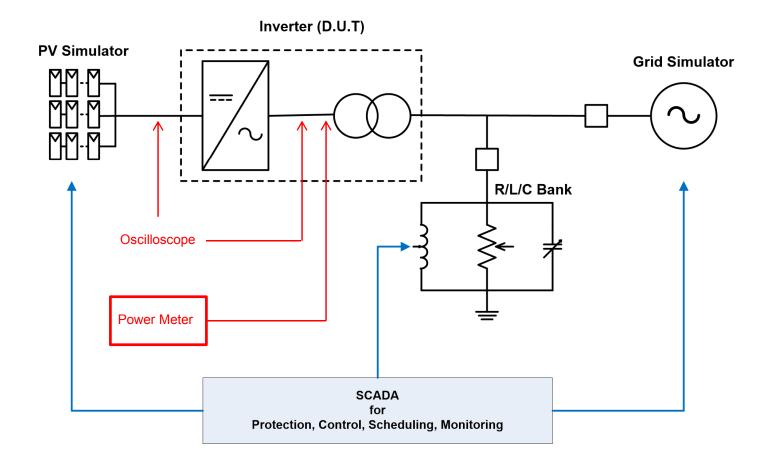
Test Setup







Test Setup Diagram







Personal Safety is No. 1 priority!







Sample Test Results





Steady State Operation with abnormal frequency

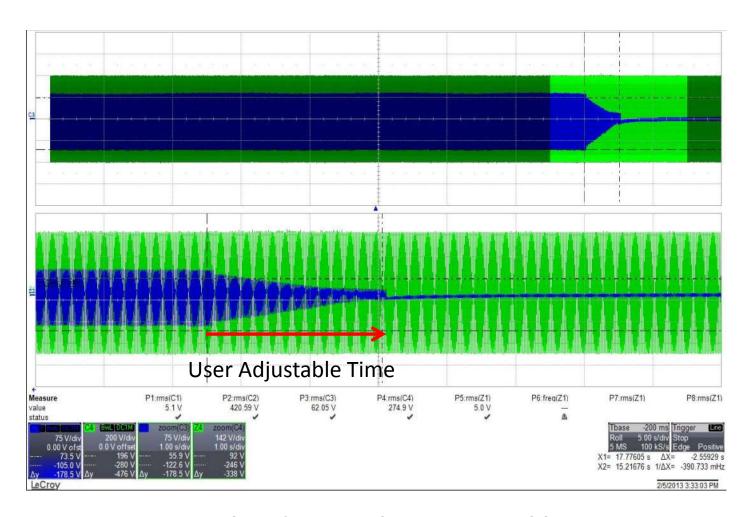


Magnetic Component operation verification





Remote Shutdown with controlled slew rate

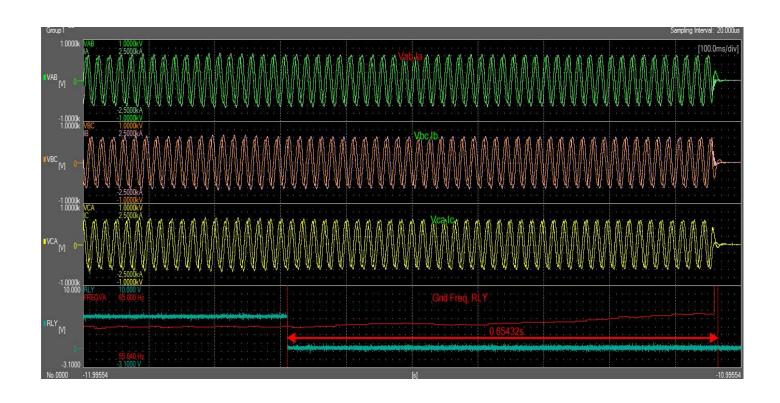


Remote shut down with programmable ramping





Protection with Grid supporting Function

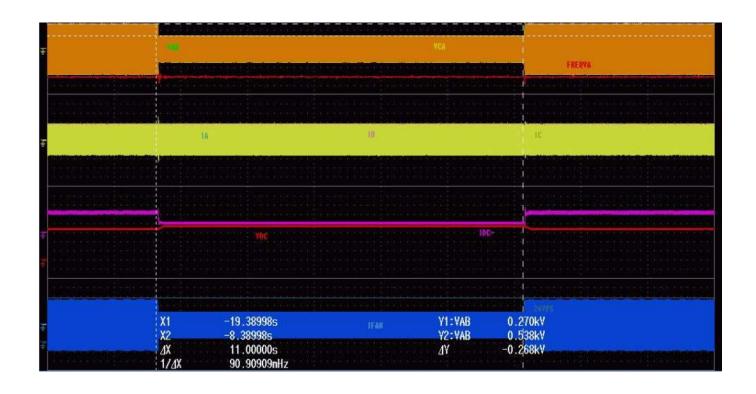


Anti-Islanding Detection while inverter operating at 0.8 PF





Functionality Test (LVRT)

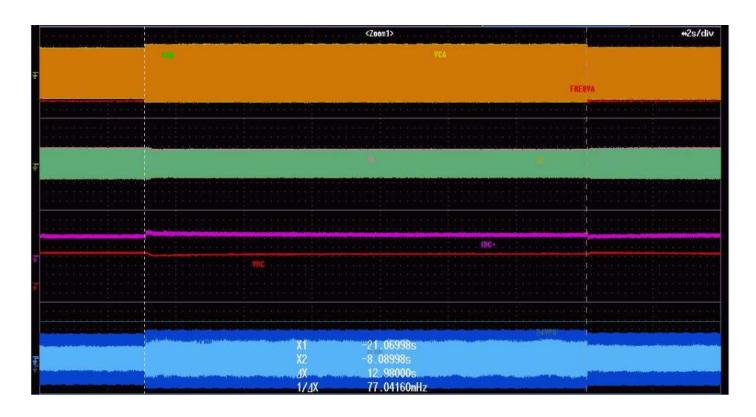


Mandatory Operation with balanced 50% sag for 11sec





Functionality Test (HVRT)

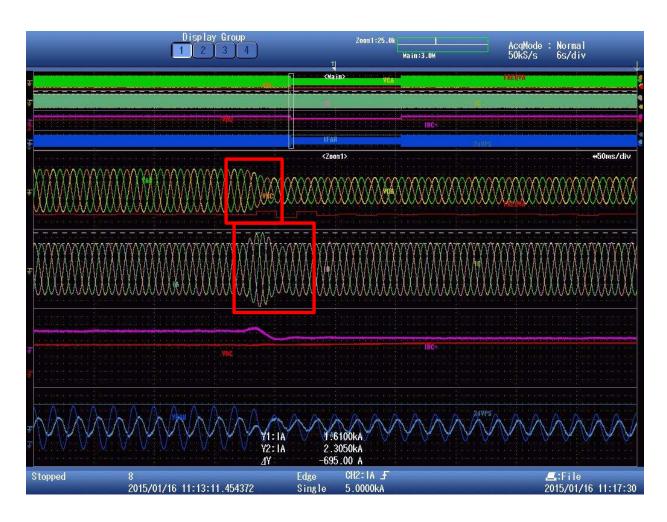


Mandatory Operation with balanced 115% swell for 13 sec





Inverter Transient Response

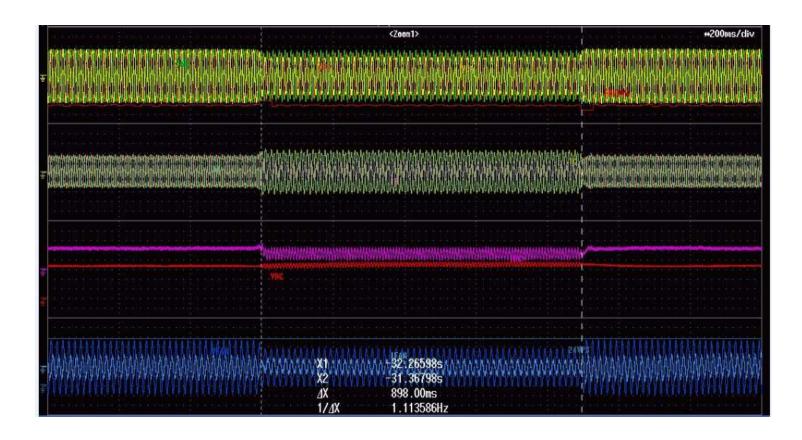


Current control loop bandwidth with 1 cycle 50% voltage sag.





Unbalanced Sag 50% for 1 sec

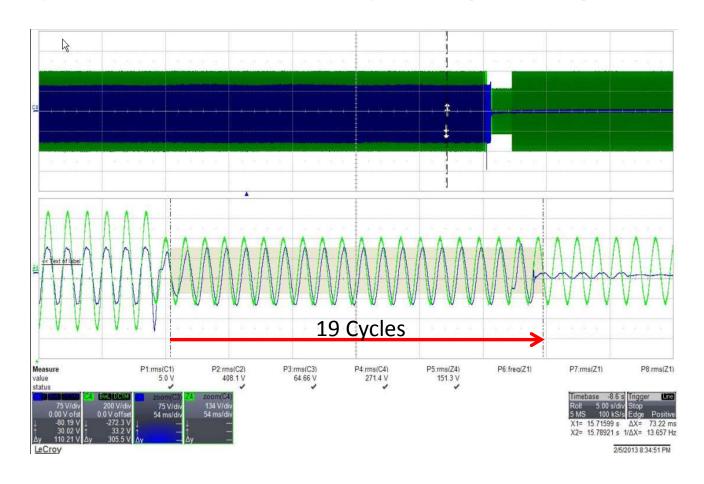


During unbalance condition, both ac and dc power is not constant, which generates ripple in the DC voltage





Trip Operation after actively riding through



Ride through for 300msec with 45%, injecting Capacitive Reactive Current





Frequency Transient Test

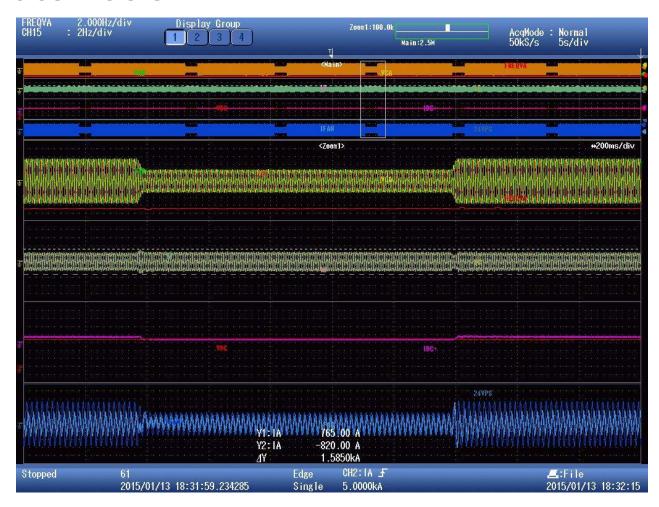


PLL Operation verification: 60Hz to 53Hz in 10 ac cycle





Test Automation

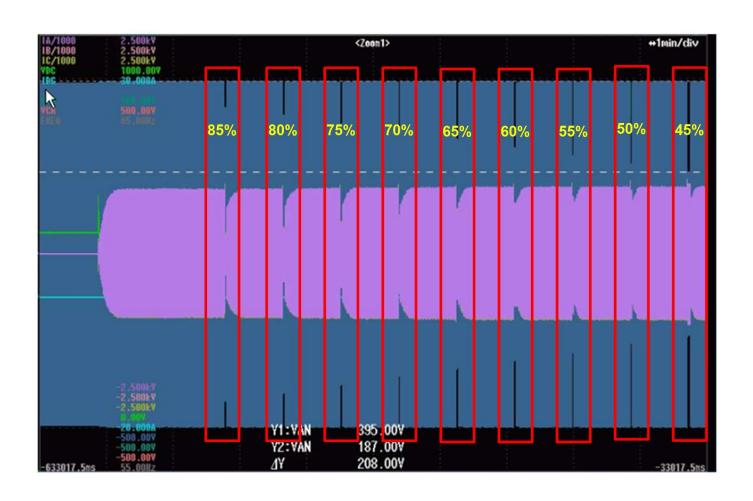


Mandatory Operation with balanced 50% sag for 11sec





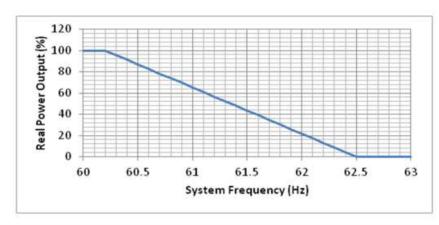
Automated test with different sag levels

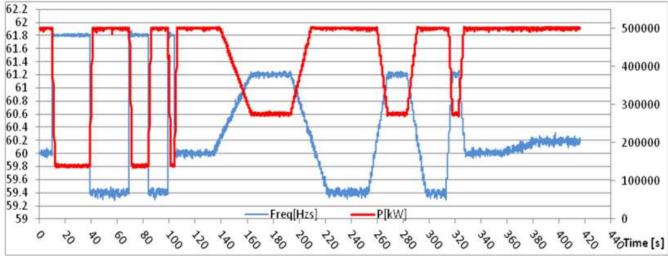






Automation for characterizing control dynamics





Freq-watt Function with Sandia Lab Test Protocol





Experience with NREL ESIF

- Great facility to test utility scale inverter based DERs available for the industry
- Timely available just before industry needs
- Technical Leadership and Support
- Great support from Coordination, Safety, Maintenance departments

Professional mindset for achievement with friendly working environment!





Expected Needs from Manufacturer's view

Hosting the certification test (with NRTL invitation) – already planned



Expanding the facility – already planned



Automated functional test: possible development with manufacturers

Automated certification test: possible DoE project for the industry





Thank you!

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